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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,655	08/21/2003	Jee-Hoon Kim	404302001300	9940

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SecuGen Corporation
2356 WALSH AVENUE
SANTA CLARA, CA 95051

EXAMINER

HASAN, MOHAMMED A

ART UNIT	PAPER NUMBER
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2873

MAIL DATE	DELIVERY MODE
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07/24/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/646,655

Applicant(s)

KIM ET AL.

Examiner

Mohammed Hasan

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period **will** apply and **will** expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply **will**, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-7,9-12,14-19 is/are rejected.
- 7) ☒ Claim(s) 8 and 13 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 21 August 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date 9/3/2004.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: ____.

DETAILED ACTION

Oath/Declaration

1. Oath and declaration filed on 2/24/2005 is accepted.

Information Disclosure Statement

2. The prior art documents submitted by applicant in the Information Disclosure Statement filed on 9/3/2004 have all been considered and made of record (note the attached copy of form PTO – 1449).

Drawings

3. The drawings are objected to because some of the figures are shadow and numeral numbers are hand written. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as “amended.” If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after

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the filing date of an application must be labeled in the top margin as either “Replacement Sheet” or “New Sheet” pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-7, 9-12, and 14-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Lee (5,895,936).

Regarding claim 1, Lee discloses (refer to figures 4-5) an image capture sensor including: a light detection transistor (TFT transistor) including a light sensitive layer which conducts electricity in response to detection of a predetermined amount of light; a switch (115) interconnected to the light detection transistor and responsive to detection of light by the light detection transistor; a glass substrate (109) layered over the light detection transistor and switch and upon which a patterned object to be imaged in placed (column 3, lines 60-67, column 4, lines 1-25).

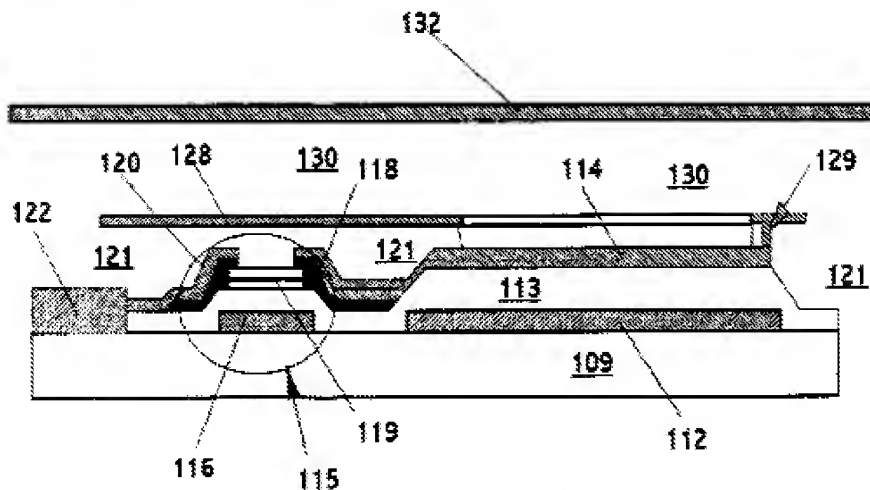


Figure 5

Regarding claim 2, Lee discloses further including a capacitor that interconnects the light detection transistor and the switch (115).

Regarding claim 3, Lee discloses wherein the switch is a transistor switch (115) (column 4, line 11).

Regarding claim 4, Lee discloses including a first light shielding layer that reduces the amount of light to which a first surface of the light sensitive layer is exposed (as shown in figure 2).

Regarding claim 5, Lee discloses wherein the glass substrate (109) includes a fiber-optic layer having fiber-optic strands formed perpendicularly to a surface of the fiber-optic layer on which an object to be imaged is placed (column 4, line 15).

Regarding claim 6, wherein the object to be imaged is a fingerprint (as shown in figure 2).

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Regarding claim 7, Lee discloses including a backlight positioned such that the light sensitive transistor and switch are positioned between the glass substrate and the backlight (as shown in figure 5).

Regarding claim 9, Lee discloses wherein the object to be imaged is a fingerprint (as shown in figure 2).

Regarding claim 10, Lee discloses (refer to figures 4-5) an image capture sensor including: a light detection transistor (TFT transistor) including a light sensitive layer which conducts electricity in response to detection of a predetermined amount of light; a switch (115) interconnected to the light detection transistor and responsive to detection of light by the light detection transistor; a glass substrate (109) layered over the light detection transistor and switch and upon which a patterned object to be imaged is placed (column 3, lines 60-67, column 4, lines 1-25).

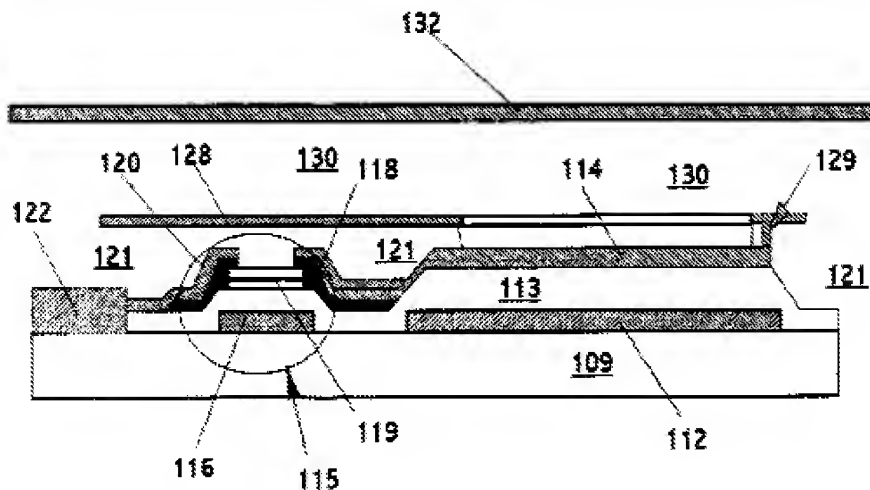


Figure 5

Regarding claim 11, Lee discloses wherein placing the object to be imaged on the glass substrate includes placing a fingerprint to be imaged on the glass substrate (109).

Regarding claim 12, Lee discloses wherein providing an image capture sensor includes providing an image capture sensor having a glass substrate (109) including fiber-optic strands.

Regarding claim 14, Lee discloses (refer to figures 4-5) an image capture sensor including: a light detection transistor (TFT transistor) including a light sensitive layer which conducts electricity in response to detection of a predetermined amount of light; a switch (115) interconnected to the light detection transistor and responsive to detection of light by the light detection transistor; a glass substrate (109) layered over the light detection transistor and switch and upon which a patterned object to be imaged

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in placed , the substrate including fiber-optic strands (column 3, lines 60-67, column 4, lines 1-25).

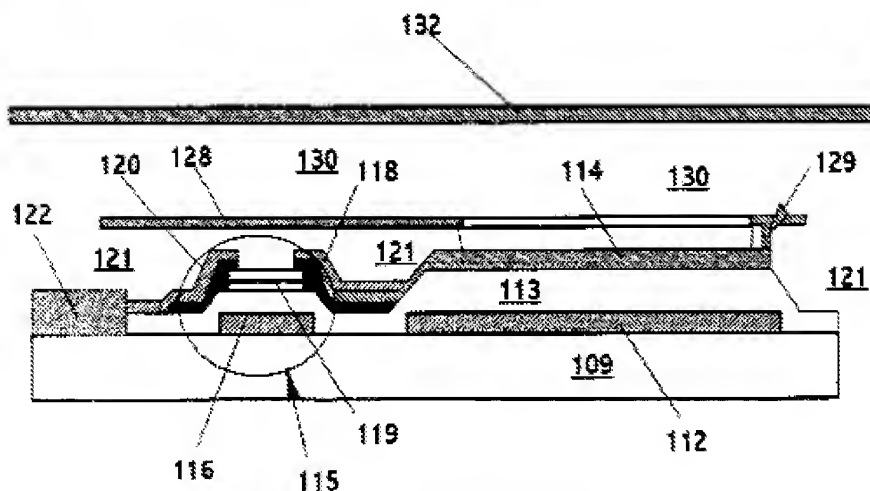


Figure 5

Regarding claim 15, Lee discloses further including a capacitor that interconnects the light detection transistor and the switch (as shown in figure 5).

Regarding claim 16, Lee discloses wherein the switch is a transistor switch (as shown in figure 5).

Regarding claim 17, Lee discloses including a first light shielding layer that reduces the amount of light to which a first surface of the light sensitive layer is exposed (as shown in figure 5).

Regarding claim 18, Lee discloses wherein the fiber-optic strands are formed perpendicularly to a surface of the substrate (as shown in figure 5).

Regarding claim 19, Lee discloses wherein the object to be imaged is a fingerprint (as shown in figure 5).

Allowable Subject Matter

5. Claim 8 and 13 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

6. The following is a statement of reasons for the indication of allowable subject matter: a conductive layer and an insulating layer, the conductive layer formed over the glass substrate and the insulating layer formed over the conductive layer such that both the conductive layer and the insulating layer are between the glass substrate and the light sensing transistor.

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The closest prior art Lee et al (6,025,599) discloses an image capturing device.

Conclusion

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mohammed Hasan whose telephone number is (571) 272-2331. The examiner can normally be reached on M-TH, 7:00 AM to 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ricky L Mack can be reached on (571) 272- 2333. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Mohammed Hasan/
Primary Examiner, Art Unit 2873
7/21/2008